

Features

- DC Characteristic Adjustable
- Transmit and Receive Gain Adjustable
- Symmetrical Input of Microphone Amplifier
- Anti-clipping in Transmit Direction
- Automatic Line-loss Compensation
- Symmetrical Output of Earpiece Amplifier
- Built-in Ear Protection
- DTMF and MUTE Input
- Adjustable Sidetone Suppression Independent of Sending and Receiving Amplification
- Speech Circuit with Two Sidetone Networks
- Built-in Line Detection Circuit
- Integrated Amplifier for Loud-hearing Operation
- Anti-clipping for Loudspeaker Amplifier
- Improved Acoustical Feedback Suppression
- Power Down
- Voice Switch
- Tone Ringer Interface with DC/DC Converter
- Zero Crossing Detection
- Common Speaker for Loud-hearing and Tone Ringer
- Supply Voltages for all Functional Blocks of a Subscriber Set
- Integrated Transistor for Short-circuiting the Line Voltage
- Answering Machine Interface
- Operation Possible from 10 mA Line Currents
- Filters against EMI on Critical I/O

Applications

- Feature Phone
- Answering Machine
- Fax Machine
- Speaker Phone

Benefits

- Savings of One Piezoelectric Transducer
- Complete System Integration of Analog Signal Processing on One Chip
- Very Few External Components
- Fewer Components for EMI Protection



**Monolithic
Integrated
Feature Phone
Circuit EMI
Improved**

U4090B-P

Rev. 4741C-CORD-11/05



1. Description

The microcontroller-controlled telephone circuit U4090B-P is a linear integrated circuit for use in feature phones, answering machines and fax machines. It contains the speech circuit, tone ringer interface with DC/DC converter, sidetone equivalent and ear protection rectifiers. The circuit is line powered and contains all components necessary for amplification of signals and adaptation to the line.

An integrated voice switch with loudspeaker amplifier allows loud-hearing or hands-free operation. With an anti-feedback function, acoustical feedback during loud-hearing can be reduced significantly. The generated supply voltage is suitable for a wide range of peripheral circuits.

Figure 1-1. Block Diagram

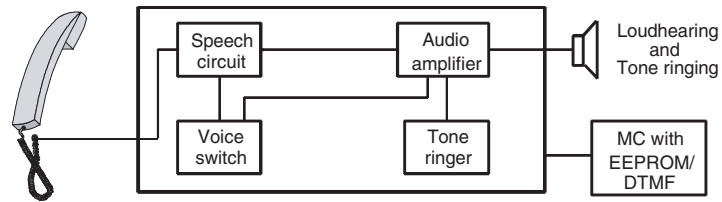
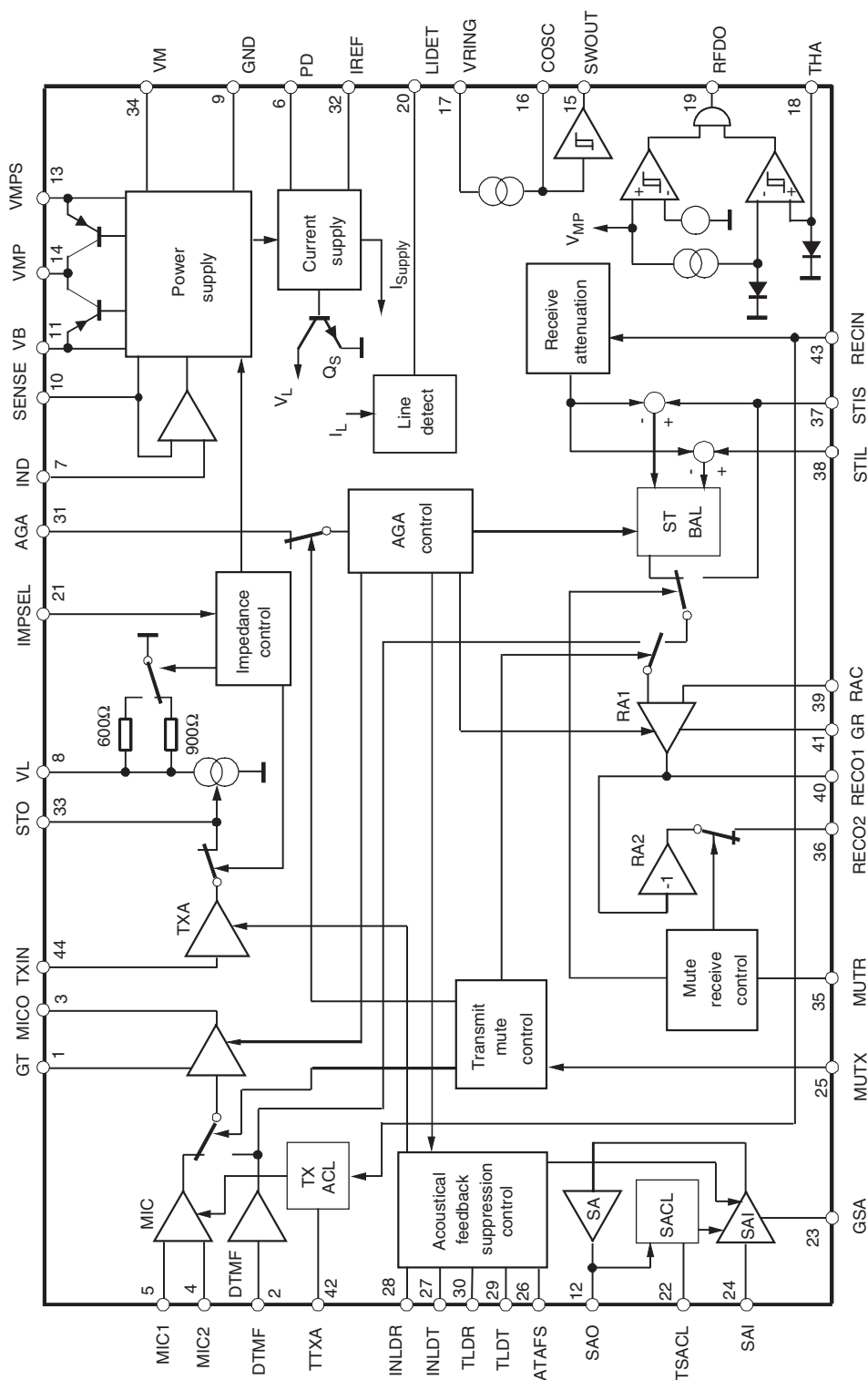


Figure 1-2. Detailed Block Diagram



2. Pin Configuration

Figure 2-1. Pinning SSO44

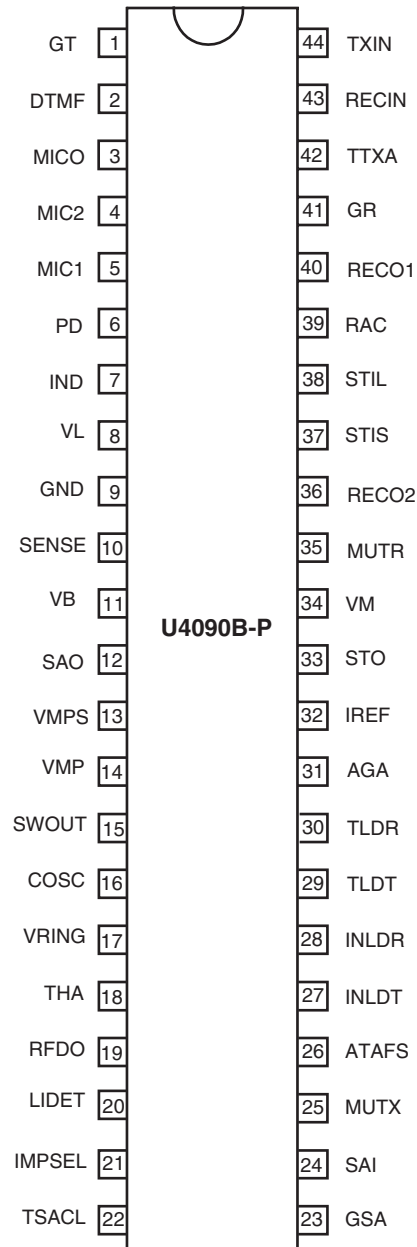


Table 2-1. Pin Description

Pin	Symbol	Function
1	GT	A resistor from this pin to GND sets the amplification of the microphone and DTMF signals, the input amplifier can be muted by applying VMP to G_T
2	DTMF	Input for DTMF signals, also used for the answering machine and hands-free input
3	MICO	Output of microphone preamplifier
4	MIC2	Non-inverting input of microphone amplifier
5	MIC1	Inverting input of microphone amplifier
6	PD	Active high input for reducing the current consumption of the circuit, simultaneously V_L is shorted by an internal switch
7	IND	The internal equivalent inductance of the circuit is proportional to the value of the capacitor at this pin, a resistor connected to ground may be used to reduce the DC line voltage
8	VL	Line voltage
9	GND	Reference point for DC- and AC-output signals
10	SENSE	A small resistor (fixed) connected from this pin to V_L sets the slope of the DC characteristic and also effects the line-length equalization characteristics and the line current at which the loudspeaker amplifier is switched on
11	VB	Unregulated supply voltage for peripheral circuits (voice switch), limited to typically 7V
12	SAO	Output of loudspeaker amplifier
13	VMPS	Unregulated supply voltage for micorcontroller, limited to 6.3V
14	VMP	Regulated supply voltage of 3.3V for peripheral circuits (especially microprocessors), minimum output current: 2 mA (ringing) 4 mA (speech mode)
15	SWOUT	Output for driving external switching transistor
16	COSC	40-kHz oscillator for ringing power converter
17	VRING	Input for ringing signal protected by internal Zener diode
18	THA	Threshold adjustment for ringing frequency detector
19	RFDO	Output of ringing frequency detector
20	LIDET	Line detect; output is low when the line current is more than 15 mA
21	IMPSEL	Control input for selection of line impedance 1. 600 Ω 2. 900 Ω 3. Mute of second transmit stage (TXA); also used for indication of external supply (answering machine); last chosen impedance is stored
22	TSACL	Time constant of anti-clipping of speaker amplifier
23	GSA	Current input for setting the gain of the speaker amplifier, adjustment characteristic is logarithmical, or $RGSA > 2\text{ M}\Omega$, the speaker amplifier is switched off
24	SA I	Speaker amplifier input (for loudspeaker, tone ringer and hands-free use)
25	MUTX	Three-state input of transmit mute: 1. Speech condition; inputs MIC1/MIC2 active 2. DTMF condition; input DTMF active. A part of the input signal is passed to the receiving amplifier as a confidence signal during dialing 3. Input DTMF used for answering machine and hands-free use; receive branch not affected
26	ATAFS	Attenuation of acoustical feedback suppression, maximum attenuation of AFS circuit is set by a resistor at this pin, without the resistor, AFS is switched off
27	INLDT	Input of transmit level detector
28	INLDR	Input of receive level detector
29	TLDT	Time constant of transmit level detector

Table 2-1. Pin Description (Continued)

Pin	Symbol	Function
30	TLDR	Time constant of receive level detector
31	AGA	Automatic gain adjustment with line current, a resistor connected from this pin to GND sets the starting point, maximum gain change: 6 dB.
32	IREF	Internal reference current generation; RREF = 62 kΩ, IREF = 20 μA
33	STO	Sidetone reduction output Output resistance approximate: 300Ω, Maximum load impedance: 10 kΩ
34	VM	Reference node for microphone-earphone and loudspeaker amplifier, supply for electret microphone (IM ≤ 700 mA)
35	MUTR	Three-state mute input 1. Normal operation 2. Mute for ear piece 3. Mute for RECIN signal Condition of earpiece mute is stored
36	RECO2	Inverting output of receiving amplifier
37	STIS	Input for sidetone network (short loop) or for answering machine
38	STIL	Input for sidetone network (long loop)
39	RAC	Input of receiving amplifier for AC coupling in feedback path
40	RECO1	Output of receiving amplifier
41	GR	A resistor connected from this pin to GND sets the receiving amplification of the circuit; amplifier RA1 can be muted by applying VMP to GR
42	TTXA	Time constant of anti-clipping in transmit path
43	RECIN	Input of receiving path; input impedance is typically 80 kΩ
44	TXIN	Input of intermediate transmit stage, input resistance is typically 20 kΩ

Note: Filters against electromagnetic interference (EMI) are located at following pins: MIC1, MIC2, RECIN, TXIN, STIS, STIL and RAC.

3. DC Line Interface and Supply-voltage Generation

The DC line interface consists of an electronic inductance and a dual-port output stage which charges the capacitors at V_{MPS} and V_B . The value of the equivalent inductance is given by:

$$L = R_{SENSE} \times C_{IND} \times ((R_{DC} \times R_{30}) / (R_{DC} + R_{30}))$$

In order to improve the supply during worst-case operating conditions, two PNP current sources - I_{BOPT} and I_{MPSOPT} - hand an extra amount of current to the supply voltages when the NPNs in parallel are unable to conduct current.

A flowchart for the control of the current sources ([Figure 3-2](#)) shows how a priority for supply VMPS is achieved.

Figure 3-1. DC Line Interface with Electronic Inductance and Generation of a Regulated and an Unregulated Supply

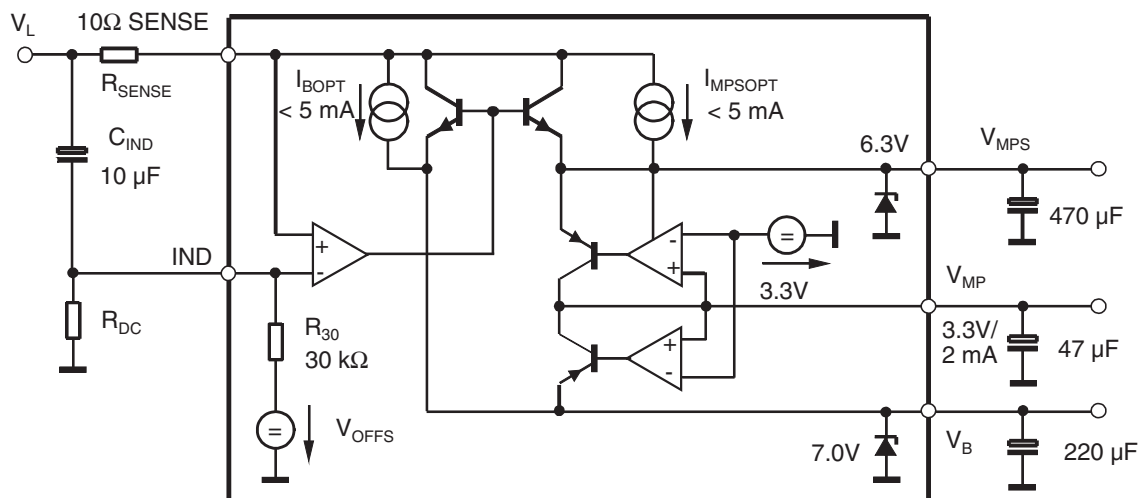


Figure 3-2. Supply Capacitors CMPS and CB Are Charged with Priority on CMPS

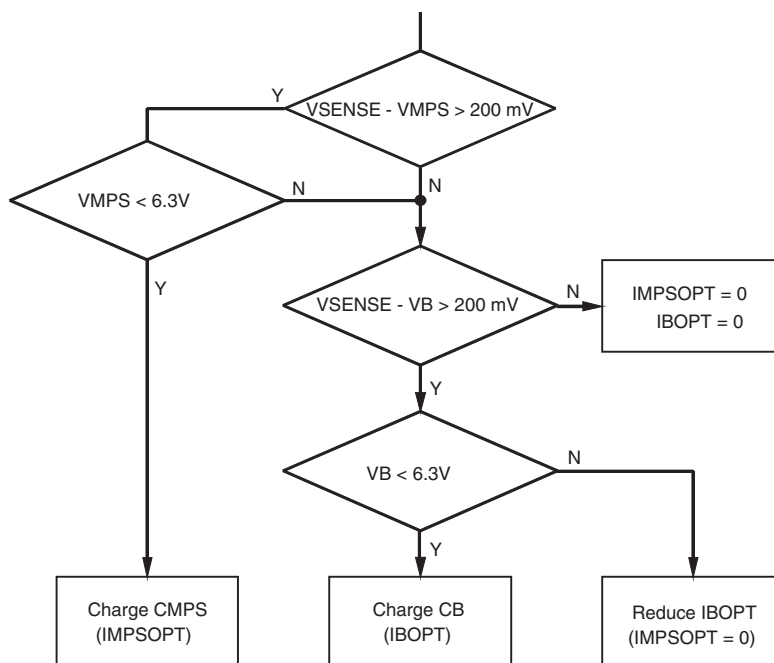
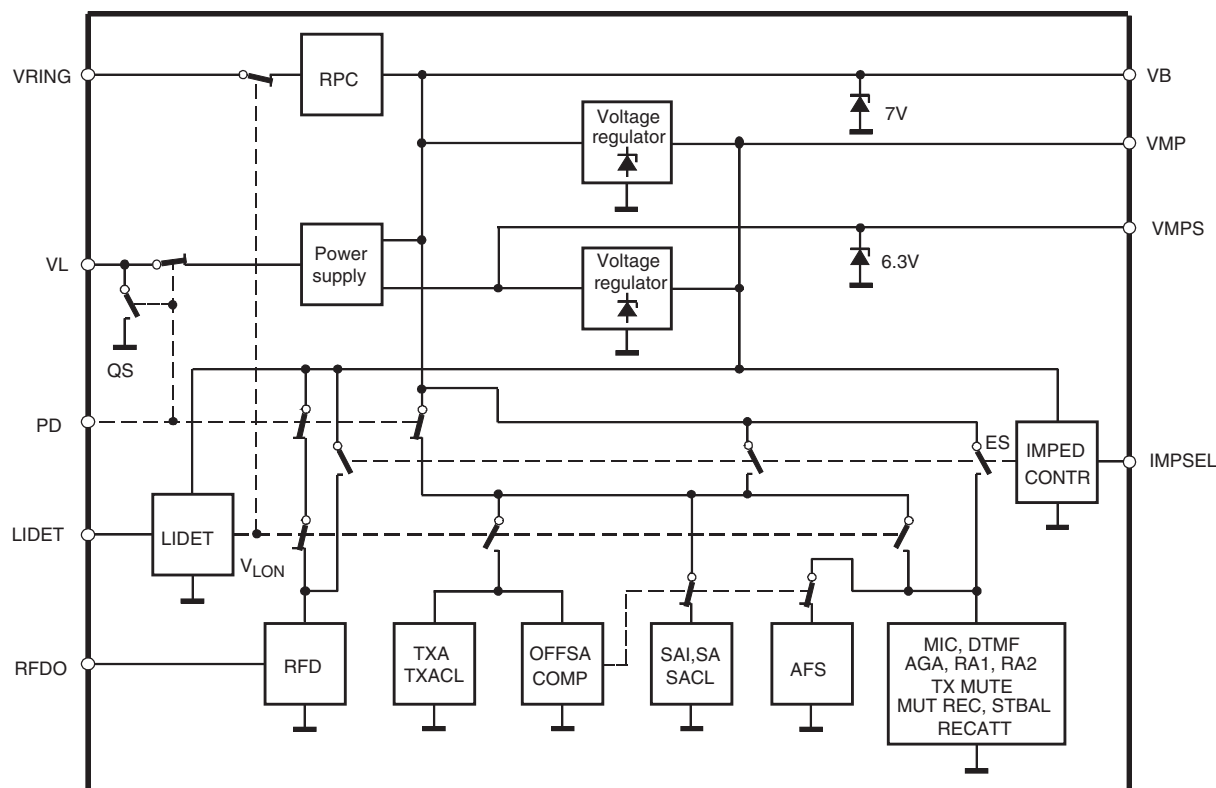


Figure 3-3. Supply of Functional Blocks Controlled by Input Voltages V_L , V_B , V_{RING} and by Logic Inputs PD and IMPSEL



The U4090B-P contains two identical series regulators which provide a supply voltage V_{MP} of 3.3V suitable for a microprocessor. In speech mode, both regulators are active because V_{MPS} and V_B are charged simultaneously by the DC-line interface. Output current is 4 mA. The capacitor at V_{MPS} is used to provide the microcomputer with sufficient power during long-line interruptions. Thus, long flash pulses can be bridged or an LCD display can be turned on for more than 2 seconds after going on hook. When the system is in ringing mode, V_B is charged by the on-chip ringing power converter. In this mode only one regulator is used to supply V_{MP} with a maximum of 2 mA.

3.1 Supply Structure of the Chip

A major benefit of the chip is that it uses a very flexible supply structure which allows simple realization of numerous applications such as:

- Group listening phone
- Hands-free phone
- Ringing with the built in speaker amplifier
- Answering machine with external supply

The special supply topology for the various functional blocks is illustrated in [Figure 3-3](#).

There are four major supply states:

1. **Speech condition:** In speech condition the system is supplied by the line current. If the LIDET-block detects a line voltage above the fixed threshold (1.9 V), the internal signal VLON is activated, thus switching off RFD and RPC and switching on all other blocks of the chip. At line voltages below 1.9V, the switches remain in their quiescent state as shown in [Figure 3-4 on page 10](#). OFFSACOMP disables the group listening feature (SAI, SA, SACL, AFS) below line currents of approximately 10 mA.
2. **Power down (pulse dialing):** When the chip is in power-down mode (PD = high), e.g., during pulse dialing, the internal switch QS shorts the line and all amplifiers are switched off. In this condition, LIDET, voltage regulators and IMPED CONTR are the only active blocks.
3. **ringing:** During ringing, the supply for the system is fed into V_B via the ringing power converter (RPC). The only functional amplifiers are in the speaker amplifier section (SAI, SA, SACL).
4. **External supply:** In an answering machine, the chip is powered by an external supply via pin V_B . This application allows the possibility to activate all amplifiers (except the transmit line interface TXA). Selecting IMPSEL = high impedance activates all switches at the ES line.

3.2 Acoustic Feedback Suppression

Acoustical feedback from the loudspeaker to the handset microphone may cause instability in the system. The U4090B-P offers a very efficient feedback suppression circuit, which uses a modified voice switch topology. [Figure 3-4 on page 10](#) shows the basic system configuration.

Two attenuators (TX ATT and RX ATT) reduce the critical loop gain by introducing an externally adjustable amount of loss either in the transmit or in the receive path. The sliding control in block ATT CONTR determines, whether the TX or the RX signal has to be attenuated. The overall loop gain remains constant under all operating conditions.

Selection of the active channel is made by comparison of the logarithmically compressed TX- and RX- envelope curve.

The system configuration for group listening, which is realized in the U4090B-P, is illustrated in [Figure 3-6 on page 11](#). TXA and SAI represent the two attenuators, the logarithmic envelope detectors are shown in a simplified way (operational amplifiers with two diodes).

Figure 3-4. Basic Voice Switch System

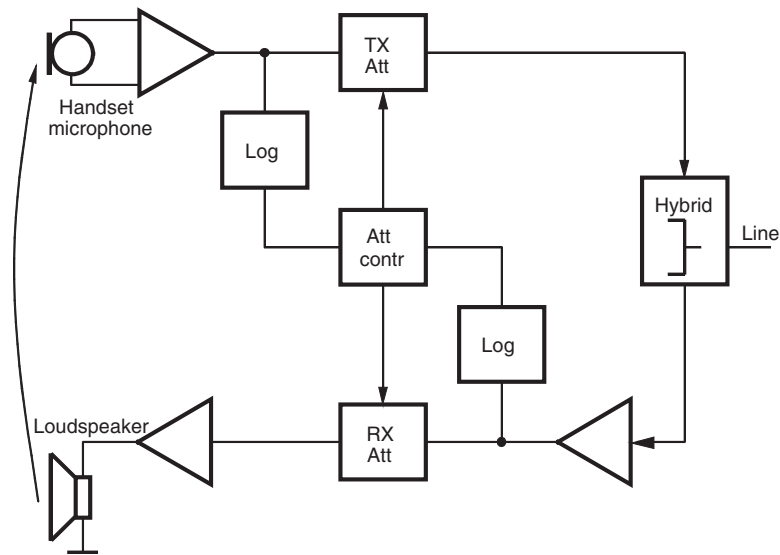


Figure 3-5. Integration of the Acoustic Feedback Suppression Circuit into the Speech Circuit Environment

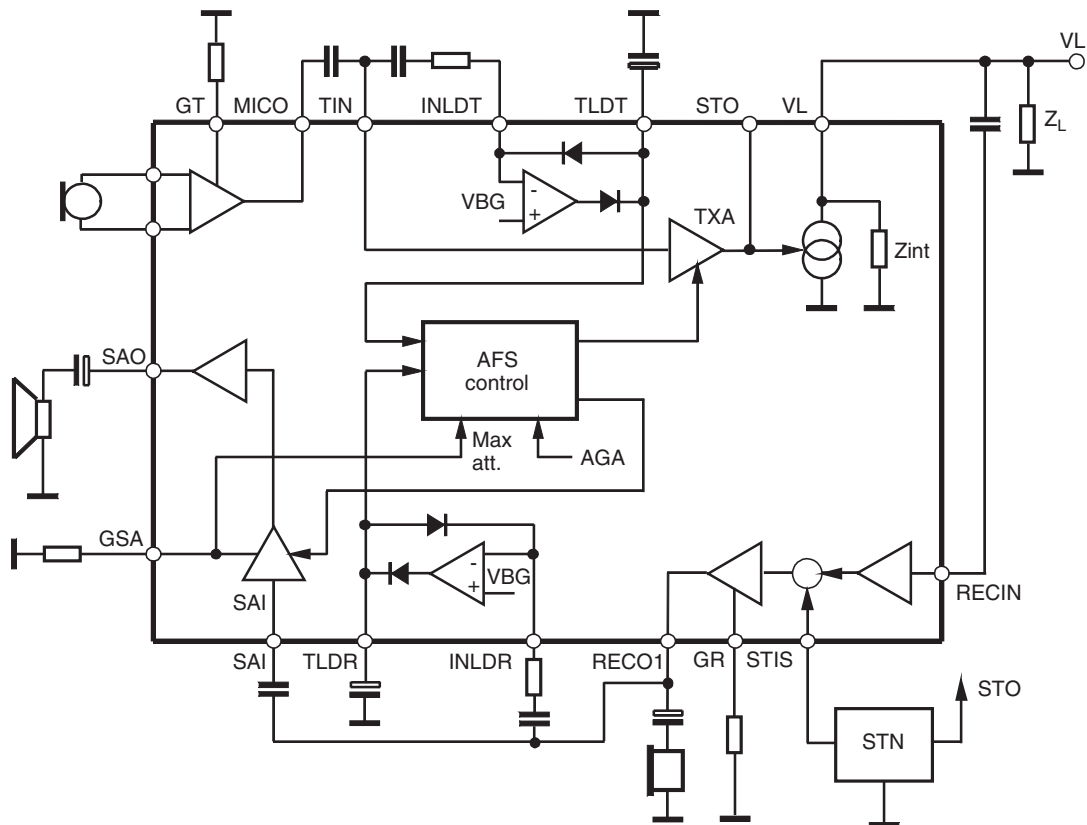
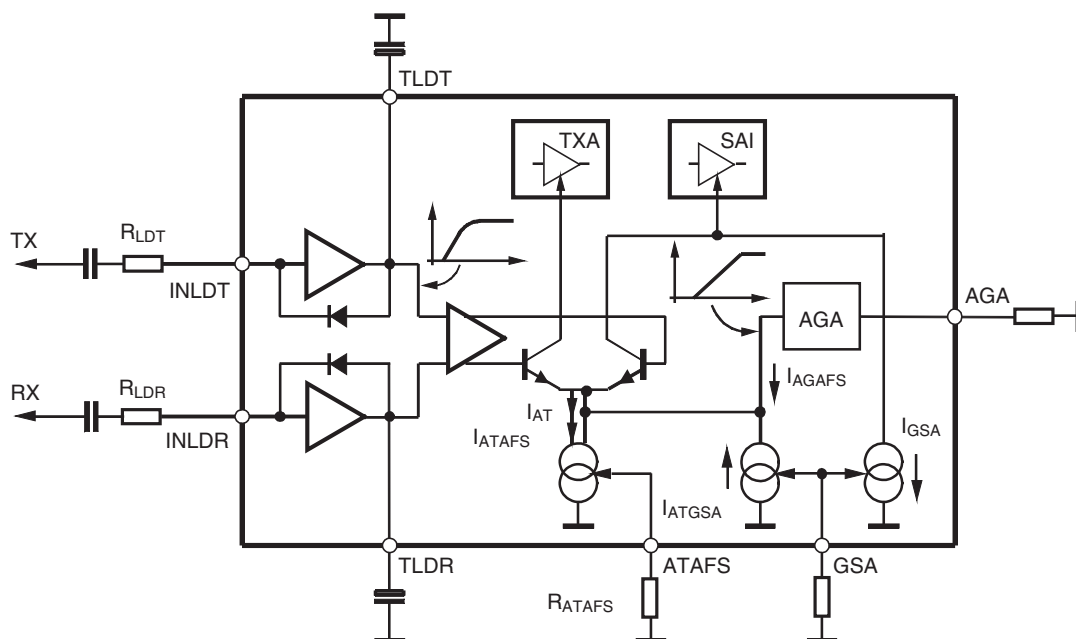


Figure 3-6. Acoustic Feedback Suppression by Alternative Control of Transmit and Speaker Amplifier Gain



A detailed diagram of the AFS (acoustic feedback suppression) is given in [Figure 3-6](#). Receive and transmit signals are first processed by logarithmic rectifiers in order to produce the envelopes of the speech at TLDT and RLDT. After amplification, a decision is made by the differential pair which direction should be transmitted.

The attenuation of the controlled amplifiers TXA and SAI is determined by the emitter current I_{AT} which consists of three parts:

I_{ATAS}	sets maximum attenuation
I_{ATGSA}	decreases the attenuation when speaker amplifier gain is reduced
I_{AGAFS}	decreases the attenuation according to the loop gain reduction caused by the AGA function

$$I_{AT} = I_{ATAFS} - I_{ATGSA} - I_{AGAFS}$$

$$\Delta G = I_{AT} \times 0.67 \text{ dB}/\mu\text{A}$$

[Figure 3-7 on page 12](#) illustrates the principle relationship between speaker amplifier gain (GSA) and attenuation of AFS (ATAFS). Both parameters can be adjusted independently, but the internal coupling between them has to be considered. The maximum usable value of GSA is 36 dB. The shape of the characteristic is moved in the x-direction by adjusting resistor R_{ATAFS} , thus changing $ATAFS_m$. The actual value of attenuation ($ATAFS_a$), however, can be determined by reading the value which belongs to the actual gain GSA_a . If the speaker amplifier gain is reduced, the attenuation of AFS is automatically reduced by the same amount in order to achieve a constant loop gain. Zero attenuation is set for speaker gains $GSA \leq GSA0 = 36 \text{ dB} - ATAFS_m$.

Figure 3-7. Reducing Speaker Amplifier Gain Results in an Equal Reduction of AFS Attenuation

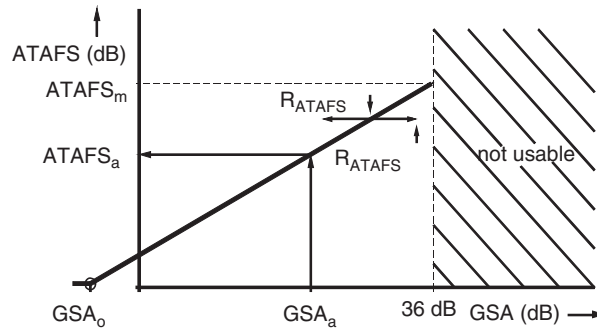
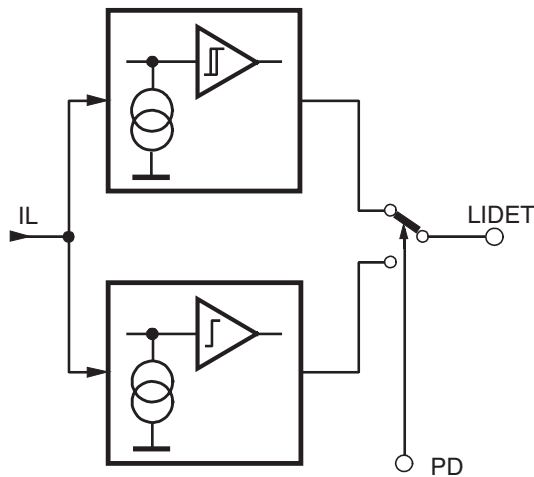


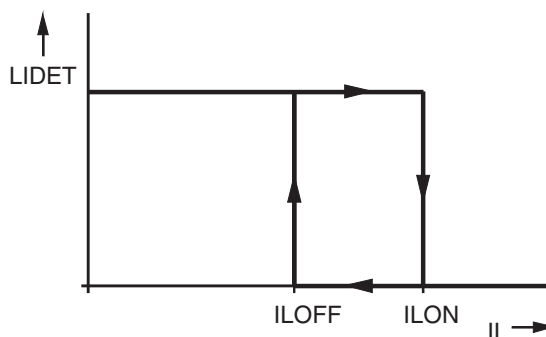
Figure 3-8. Line Detection with Two Comparators for Speech Mode and Pulse Dialing



3.3 Line Detection (LIDET)

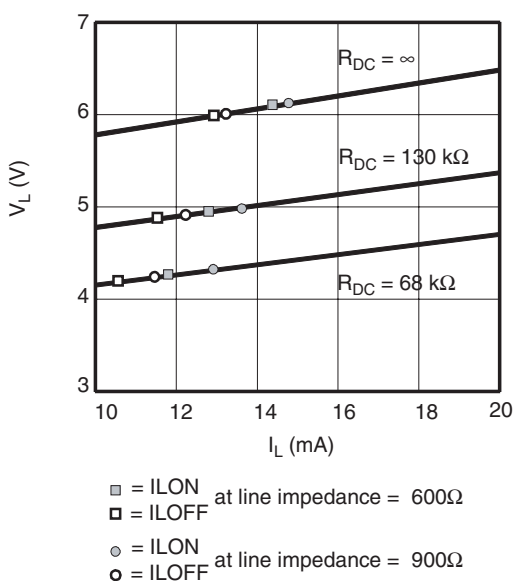
The line current supervision is active under all operating conditions of the U4090B-P. In speech mode (PD = inactive), the line-current comparator uses the same thresholds as the comparator for switching off the entire speaker amplifier. The basic behavior is illustrated in [Figure 3-9 on page 13](#). Actual values of ILON/ILOFF vary slightly with the adjustment of the DC characteristics and the selection of the internal line impedance.

When Power Down is activated (during pulse dialing), the entire line current flows through the short-circuiting transistor QS (see [Figure 3-3 on page 8](#)). As long as IL is above typically 1.6 mA, output LIDET is low. This comparator does not use hysteresis.

Figure 3-9. Line Detection in Speech Mode with Hysteresis

3.4 Ringing Power Converter (RPC)

The RPC transforms the input power at VRING (high voltage/low current) into an equivalent output power at V_B (low voltage/high current) which is capable of driving the low-ohmic loudspeaker. Input impedance at VRING is fixed at 5 k Ω and the efficiency of the step-down converter is approximate 65%.

Figure 3-10. Comparator Thresholds Depending on DC Mask and Line Impedance

3.5 Ringing Frequency Detector (RFD)

The U4090B-P offers an output signal for the microcontroller, which is a digital representation of the double ringing frequency. It is generated by a current comparator with hysteresis. The input voltage V_{RING} is transformed into a current via RTHA. The thresholds are 8 μ A and 24 μ A. RFDO and V_{RING} are in phase. A second comparator with hysteresis is used to enable the output RFDO as long as the supply voltage for the microprocessor VMP is above 2.0V.

4. Absolute Maximum Ratings

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Parameters	Symbol	Value	Unit
Line current	I_L	140	mA
DC line voltage	V_L	12	V
Maximum input current, pin 17	I_{RING}	15	mA
Junction temperature	T_j	125	°C
Ambient temperature	T_{amb}	-25 to +75	°C
Storage temperature	T_{stg}	-55 to +150	°C
Total power dissipation, $T_{amb} = 60^\circ\text{C}$	P_{tot}	0.9	W

5. Thermal Resistance

Parameters	Symbol	Value	Unit
Junction ambient SSO44	R_{thJA}	70	K/W

6. Electrical Characteristics

$f = 1 \text{ kHz}$, $0 \text{ dBm} = 775 \text{ mV}_{rms}$, $I_M = 0.3 \text{ mA}$, $I_{MP} = 2 \text{ mA}$, $R_{DC} = 130 \text{ k}\Omega$, $T_{amb} = 25^\circ\text{C}$, $R_{GSA} = 560 \text{ k}\Omega$, $Z_{ear} = 68 \text{ nF} + 100\Omega$, $Z_M = 68 \text{ nF}$, pin 31 open, $V_{IMPSEL} = \text{GND}$, $V_{MUTX} = \text{GND}$, $V_{MUTR} = \text{GND}$, unless otherwise specified.

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
DC Characteristics						
DC voltage drop over circuit	$I_L = 2 \text{ mA}$ $I_L = 14 \text{ mA}$ $I_L = 60 \text{ mA}$ $I_L = 100 \text{ mA}$	V_L	4.6 8.8	2.4 5.0 7.5 9.4	5.4 10.0	V
Transmission Amplifier, $I_L = 14 \text{ mA}$, $V_{MIC} = 2 \text{ mV}$, $R_{GT} = 27 \text{ k}\Omega$, Unless Otherwise Specified						
Range of transmit gain		G_T	40	45	50	dB
Transmitting amplification	$R_{GT} = 12 \text{ k}\Omega$ $R_{GT} = 27 \text{ k}\Omega$	G_T	47 39.8	48	49 41.8	dB
Frequency response	$I_L \geq 14 \text{ mA}$ $f = 300 \text{ to } 3400 \text{ Hz}$	ΔG_T			± 0.5	dB
Gain change with current	Pin 31 open $I_L = 14 \text{ to } 100 \text{ mA}$	ΔG_T			± 0.5	dB
Gain deviation	$T_{amb} = -10 \text{ to } +60^\circ\text{C}$	ΔG_T			± 0.5	dB
CMRR of microphone amplifier		CMRR	60	80		dB
Input resistance of MIC amplifier	$R_{GT} = 12 \text{ k}\Omega$ $R_{GT} = 27 \text{ k}\Omega$	R_i	45	50 75	110	$\text{k}\Omega$
Distortion at line	$I_L > 14 \text{ mA}$ $V_L = 700 \text{ mV}_{rms}$	d_t			2	%
Maximum output voltage	$I_L > 19 \text{ mA}$, $d < 5\%$ $V_{MIC} = 25 \text{ mV}$ $CTXA = 1 \mu\text{F}$	V_{Lmax}	1.8	3	4.2	dBm
	IMPSEL = open $R_{GT} = 12 \text{ k}\Omega$	$V_{MIComax}$		-5.2		dBm

6. Electrical Characteristics (Continued)

$f = 1 \text{ kHz}$, $0 \text{ dBm} = 775 \text{ mV}_{\text{rms}}$, $I_M = 0.3 \text{ mA}$, $I_{MP} = 2 \text{ mA}$, $RDC = 130 \text{ k}\Omega$, $T_{\text{amb}} = 25^\circ\text{C}$, $RGSA = 560 \text{ k}\Omega$, $Z_{\text{ear}} = 68 \text{ nF} + 100\Omega$, $Z_M = 68 \text{ nF}$, pin 31 open, $V_{\text{IMPSEL}} = \text{GND}$, $V_{\text{MUTX}} = \text{GND}$, $V_{\text{MUTR}} = \text{GND}$, unless otherwise specified.

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Noise at line psophometrically weighted	$I_L > 14 \text{ mA}$ $G_T = 48 \text{ dB}$	no		-80	-72	dBmp
Anti-clipping attack time release time	$CTXA = 1 \mu\text{F}$ each 3 dB overdrive			0.5 9		ms
Gain at low operating current	$I_L = 10 \text{ mA}$ $I_{MP} = 1 \text{ mA}$ $RDC = 68 \text{ k}\Omega$ $V_{\text{MIC}} = 1 \text{ mV}$ $I_M = 300 \mu\text{A}$	G_T	40		42.5	dB
Distortion at low operating current	$I_L = 10 \text{ mA}$ $I_M = 300 \mu\text{A}$ $I_{MP} = 1 \text{ mA}$ $RDC = 68 \text{ k}\Omega$ $V_{\text{MIC}} = 10 \text{ mV}$	d_i			5	%
Line loss compensation	$I_L = 100 \text{ mA}$ $R_{\text{AGA}} = 20 \text{ k}\Omega$	ΔG_{TI}	-6.4	-5.8	-5.2	dB
Mute suppression a) MIC muted (microphone preamplifier) b) TXA muted (second stage)	$I_L \geq 14 \text{ mA}$ $\text{MUTX} = \text{open}$	G_{TM}	60	80		dB
	$\text{IMPSEL} = \text{open}$	G_{TTX}	60			dB
Receiving Amplifier, $I_L = 14 \text{ mA}$, $R_{\text{GR}} = 62 \text{ k}\Omega$, Unless Otherwise Specified, $V_{\text{GEN}} = 300 \text{ mV}$						
Adjustment range of receiving gain	$I_L \geq 14 \text{ mA}$, single ended differential $\text{MUTR} = \text{GND}$	G_R	-8 -2		+2 +8	dB
Receiving amplification	$R_{\text{GR}} = 62 \text{ k}\Omega$ differential $R_{\text{GR}} = 22 \text{ k}\Omega$ differential	G_R	-1.75	-1 7.5	-0.25	dB
Amplification of DTMF signal from DTMF IN to RECO 1, 2	$I_L \geq 14 \text{ mA}$ $V_{\text{MUTX}} = V_{\text{MP}}$	G_{RM}	7	10	13	dB
Frequency response	$I_L > 14 \text{ mA}$, $f = 300 \text{ to } 3400 \text{ Hz}$	ΔG_{RF}			± 0.5	dB
Gain change with current	$I_L = 14 \text{ to } 100 \text{ mA}$	ΔG_R			± 0.5	dB
Gain deviation	$T_{\text{amb}} = -10 \text{ to } +60^\circ\text{C}$	ΔG_R			± 0.5	dB
Ear-protection differential	$I_L \geq 14 \text{ mA}$, $V_{\text{GEN}} = 11 V_{\text{rms}}$	EP			2.2	V_{rms}
MUTE suppression a) RECAT b) RA2 c) DTMF operation	$I_L \geq 14 \text{ mA}$ $\text{MUTR} = \text{open}$ $V_{\text{MUTR}} = V_{\text{MP}}$ $V_{\text{MUTX}} = V_{\text{MP}}$	ΔG_R	60			dB
Output voltage $d \leq 2\%$ differential	$I_L = 14 \text{ mA}$, $Z_{\text{ear}} = 68 \text{ nF} + 100\Omega$		0.775			V_{rms}
Maximum output current $d \leq 2\%$	$Z_{\text{ear}} = 100\Omega$		4			mA (peak)
Receiving noise psophometrically weighted	$Z_{\text{ear}} = 68 \text{ nF} + 100\Omega$ $I_L \geq 14 \text{ mA}$	ni		-80	-77	dBmp
Output resistance	Each output against GND	R_o			10	Ω
Line loss compensation	$R_{\text{AGA}} = 20 \text{ k}\Omega$, $I_L = 100 \text{ mA}$	ΔG_{RI}	-7.0	-6.0	-5.0	dB

6. Electrical Characteristics (Continued)

$f = 1 \text{ kHz}$, $0 \text{ dBm} = 775 \text{ mV}_{\text{rms}}$, $I_M = 0.3 \text{ mA}$, $I_{MP} = 2 \text{ mA}$, $RDC = 130 \text{ k}\Omega$, $T_{\text{amb}} = 25^\circ\text{C}$, $R_{GSA} = 560 \text{ k}\Omega$, $Z_{\text{ear}} = 68 \text{ nF} + 100\Omega$, $Z_M = 68 \text{ nF}$, pin 31 open, $V_{\text{IMPSEL}} = \text{GND}$, $V_{\text{MUTX}} = \text{GND}$, $V_{\text{MUTR}} = \text{GND}$, unless otherwise specified.

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Gain at low operating current	$I_L = 10 \text{ mA}$ $I_{MP} = 1 \text{ mA}$ $I_M = 300 \mu\text{A}$ $V_{\text{GEN}} = 560 \text{ mV}$ $RDC = 68 \text{ k}\Omega$	G_R	-2	-1	0	dB
AC impedance	$V_{\text{IMPSEL}} = \text{GND}$ $V_{\text{IMPSEL}} = V_{MP}$	Z_{imp} Z_{imp}	570 840	600 900	640 960	Ω Ω
Distortion at low operating current	$I_L = 10 \text{ mA}$ $I_{MP} = 1 \text{ mA}$ $V_{\text{GEN}} = 560 \text{ mV}$ $RDC = 68 \text{ k}\Omega$	dR			5	%
Speaker Amplifier						
Minimum line current for operation	No AC signal	$I_{L\text{min}}$			15	mA
Input resistance	Pin 24		14		22	k Ω
Gain from SAI to SAO	$V_{\text{SAI}} = 3 \text{ mV}$ $I_L = 15 \text{ mA}$ $R_{GSA} = 560 \text{ k}\Omega$ $R_{GSA} = 20 \text{ k}\Omega$	G_{SA}	35.5	36.5 -3	37.5	dB
Output power	Load resistance $R_L = 50\Omega$, $d < 5\%$ $V_{\text{SAI}} = 20 \text{ mV}$ $I_L = 15 \text{ mA}$ $I_L = 20 \text{ mA}$	P_{SA} P_{SA}	3	7 20		mW
Output noise (Input SAI open) psophometrically weighted	$I_L > 15 \text{ mA}$	n_{SA}			200	$\mu\text{V}_{\text{psoph}}$
Gain deviation	$I_L = 15 \text{ mA}$, $T_{\text{amb}} = -10 \text{ to } +60^\circ\text{C}$	ΔG_{SA}			± 1	dB
Mute suppression	$I_L = 15 \text{ mA}$ $V_L = 0 \text{ dBm}$ $V_{\text{SAI}} = 4 \text{ mV}$ Pin 23 open	V_{SAO}			-60	dBm
Gain change with current	$I_L = 15 \text{ to } 100 \text{ mA}$	ΔG_{SA}			± 1	dB
Resistor for turning off speaker amplifier	$I_L = 15 \text{ to } 100 \text{ mA}$	R_{GSA}	0.8	1.3	2	M Ω
Gain change with frequency	$I_L = 15 \text{ mA}$, $f = 300 \text{ to } 3400 \text{ Hz}$	ΔG_{SA}			± 0.5	dB
Attack time of anti-clipping	20 dB over drive	tr		5		ms
Release time of anti-clipping		tf		80		ms
DTMF Amplifier Test Conditions: $I_{MP} = 2 \text{ mA}$, $I_M = 0.3 \text{ mA}$, $V_{\text{MUTX}} = V_{MP}$						
Adjustment range of DTMF gain	$I_L = 15 \text{ mA}$ mute active	G_D	40		50	dB
DTMF amplification	$I_L = 15 \text{ mA}$ $V_{\text{DTMF}} = 8 \text{ mV}$ Mute active: $\text{MUTX} = V_{MP}$	G_D	40.7	41.7	42.7	dB
Gain deviation	$I_L = 15 \text{ mA}$, $T_{\text{amb}} = -10 \text{ to } +60^\circ\text{C}$	G_D			± 0.5	dB
Input resistance	$R_{GT} = 27 \text{ k}\Omega$ $R_{GT} = 15 \text{ k}\Omega$	R_i	60 26	180 70	300 130	k Ω
Distortion of DTMF signal	$I_L \geq 15 \text{ mA}$ $V_L = 0 \text{ dBm}$	d_D			2	%

6. Electrical Characteristics (Continued)

$f = 1 \text{ kHz}$, $0 \text{ dBm} = 775 \text{ mV}_{\text{rms}}$, $I_M = 0.3 \text{ mA}$, $I_{MP} = 2 \text{ mA}$, $RDC = 130 \text{ k}\Omega$, $T_{\text{amb}} = 25^\circ\text{C}$, $RGSA = 560 \text{ k}\Omega$, $Z_{\text{ear}} = 68 \text{ nF} + 100\Omega$, $Z_M = 68 \text{ nF}$, pin 31 open, $V_{\text{IMPSEL}} = \text{GND}$, $V_{\text{MUTX}} = \text{GND}$, $V_{\text{MUTR}} = \text{GND}$, unless otherwise specified.

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Gain deviation with current	$I_L = 15 \text{ to } 100 \text{ mA}$	ΔG_D			± 0.5	dB
AFS Acoustic Feedback Suppression						
Adjustment range of attenuation	$I_L \geq 15 \text{ mA}$		0		50	dB
Attenuation of transmit gain	$I_L \geq 15 \text{ mA}$ $I_{\text{INLDT}} = 0 \text{ }\mu\text{A}$ $R_{\text{ATAFS}} = 30 \text{ k}\Omega$ $I_{\text{INLDR}} = 10 \text{ }\mu\text{A}$	ΔG_T		45		dB
Attenuation of speaker amplifier	$I_L \geq 15 \text{ mA}$ $I_{\text{INLDP}} = 0 \text{ }\mu\text{A}$ $R_{\text{ATAFS}} = 30 \text{ k}\Omega$ $I_{\text{INLDR}} = 10 \text{ }\mu\text{A}$	ΔG_{SA}		50		dB
AFS disable	$I_L \geq 15 \text{ mA}$	V_{ATAFS}	1.5			V
Supply Voltages, $V_{\text{MIC}} = 25 \text{ mV}$, $T_{\text{amb}} = -10 \text{ to } +60^\circ\text{C}$						
V_{MP}	$I_L = 14 \text{ mA}$ $RDC = 68 \text{ k}\Omega$ $I_{\text{MP}} = 2 \text{ mA}$	V_{MP}	3.1	3.3	3.5	V
V_{MPS}	$I_L = 100 \text{ mA}$ $RDC = \text{infinite}$ $I_{\text{MP}} = 0 \text{ mA}$	V_{MPS}			6.7	V
V_M	$I_L \geq 14 \text{ mA}$ $I_M = 700 \text{ }\mu\text{A}$ $RDC = 130 \text{ k}\Omega$	V_M	1.3		3.3	V
V_B	$I_B = 20 \text{ mA}$ $I_L = 0 \text{ mA}$	V_B		7	7.6	V
Ring Power Converter, $I_{\text{MP}} = 1 \text{ mA}$, $I_M = 0$						
Maximum output power	$V_{\text{RING}} = 20.6\text{V}$	P_{SA}		20		mW
Threshold of ring frequency detector	RFDO: low to high $V_{\text{HYST}} = V_{\text{RINGON}} - V_{\text{RINGOFF}}$	V_{RINGON} V_{HYST}		17.5 11.0		V
Input impedance	$V_{\text{RING}} = 30\text{V}$	R_{RING}	4	5	6	k Ω
Input impedance in speech mode	$f = 300 \text{ Hz to } 3400 \text{ Hz}$, $I_L > 15 \text{ mA}$ $V_{\text{RING}} = 20\text{V} + 1.5V_{\text{rms}}$	R_{RINGSP}	150			k Ω
Logic level of frequency detector	$V_{\text{RING}} = 0\text{V}$ $V_B = 4\text{V}$ $V_{\text{RING}} = 25\text{V}$	V_{RFDO}		0 V_{MP}		V
Ring detector enable	$V_{\text{RING}} = 25\text{V}$, RFDO high	V_{MPON}	1.8	2.0	2.2	V
Zener diode voltage	$I_{\text{RING}} = 25 \text{ mA}$	V_{RINGmax}	30.8		33.3	V
MUTR Input						
MUTR input current	$V_{\text{MUTR}} = \text{GND}$ $I_L > 14 \text{ mA}$ $V_{\text{MUTR}} = V_{\text{MP}}$	I_{MUTE}		-20 +10	-30	μA
MUTR input voltage	Mute low; $I_L > 14 \text{ mA}$	V_{MUTE}			0.3	V
	Mute high; $I_L > 14 \text{ mA}$	V_{MUTE}	$V_{\text{MP}} - 0.3\text{V}$			V

6. Electrical Characteristics (Continued)

$f = 1 \text{ kHz}$, $0 \text{ dBm} = 775 \text{ mV}_{\text{rms}}$, $I_M = 0.3 \text{ mA}$, $I_{MP} = 2 \text{ mA}$, $RDC = 130 \text{ k}\Omega$, $T_{\text{amb}} = 25^\circ\text{C}$, $RGSA = 560 \text{ k}\Omega$, $Z_{\text{ear}} = 68 \text{ nF} + 100\Omega$, $Z_M = 68 \text{ nF}$, pin 31 open, $V_{\text{IMPSEL}} = \text{GND}$, $V_{\text{MUTX}} = \text{GND}$, $V_{\text{MUTR}} = \text{GND}$, unless otherwise specified.

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
PD Input						
PD input current	PD active, $I_L > 14 \text{ mA}$ $V_{PD} = V_{MP}$	I_{pd}		9		μA
Input voltage	PD = active PD = inactive	V_{pd} V_{pd}	2		0.3	V
Voltage drop at V_L	$I_L = 14 \text{ mA}$, PD = active $I_L = 100 \text{ mA}$, PD = active	V_L		1.5 1.9		V
Input Characteristics of IMPSEL						
Input current	$I_L \geq 14 \text{ mA}$ $V_{\text{IMPSEL}} = V_{MP}$ $V_{\text{IMPSEL}} = \text{GND}$	I_{IMPSEL} I_{IMPSEL}		18 -18		μA μA
Input voltage	Input high	V_{IMPSEL}	$V_{MP} - 0.3\text{V}$			V
	Input low	V_{IMPSEL}			0.3	V
MUTX Input						
Input current	$V_{\text{MUTX}} = V_{MP}$ $V_{\text{MUTX}} = \text{GND}$	I_{MUTX} I_{MUTX}		20 -20	30 -30	μA μA
Input voltage	Input high	V_{MUTX}	$V_{MP} - 0.3\text{V}$			V
	Input low	V_{MUTX}			0.3	V
Line Detection						
Line current for LIDET active	PD = inactive	ILON		12.6		mA
Line current for LIDET inactive	PD = inactive	ILOFF		11.0		mA
Current threshold during power down	$V_B = 5\text{V}$, PD = active	ILONPD	0.8	1.6	2.4	mA

7. U4090B-P Control

Table 7-1. Selection of TX Mute and Line Impedance

Logic Level	IMPSEL	MODE
0	Line impedance = 600Ω TXA = on ES = off	Speech
0 to Z	Line impedance = 600Ω TXA = off ES = on	Transmit-mute
1 to Z	Line impedance = 900Ω TXA = off ES = on	Transmit-mute
1	Line impedance = 900Ω TXA = on ES = off	Speech

Table 7-2. Selection of Earpiece Mute and Answering Machine Mode

Logic Level	MUTR	MODE
0	RA2 = on RECAT = on STIS + STIL = on	Speech
0 to Z	RA2 = on RECAT = off STIS = on, STIL = off	For answering machine
1 to Z	RA2 = off RECAT = off STIS = on, STIL = off AGA off for STIS	For answering machine
1	RA2 = off RECAT = on STIS + STIL = on	Speech + earpiece mute

Table 7-3. Selection of Transmit Mute

Logic Level	MUTX	MODE
0	MIC 1/2 transmit enabled receive enable AFS = on AGA = on TXACL = on	Speech
Z	DTMF transmit enabled receive enable AFS = on AGA = on TXACL = on	For answering machine
1	DTMF transmit enabled DTMF to receive enable AFS = off AGA = off TXACL = off	DTMF dialling

Table 7-4. Specification of Logic Levels

Logic Level
0 = < (0.3V)
Z = > (1V) < (VMP – 1V) or (open input)
1 = > (VMP – 0.3V)

8. Explanation of Abbreviations

RECATT = Receive attenuation
 STIS, STIL = Inputs of sidetone balancing amplifiers
 ES = External supply
 AFS = Acoustic feedback suppression control
 AGA = Automatic gain adjustment
 RA2 = Inverting receive amplifier
 TXACL = Transmit anti-clipping control

Figure 8-1. Typical DC Characteristic

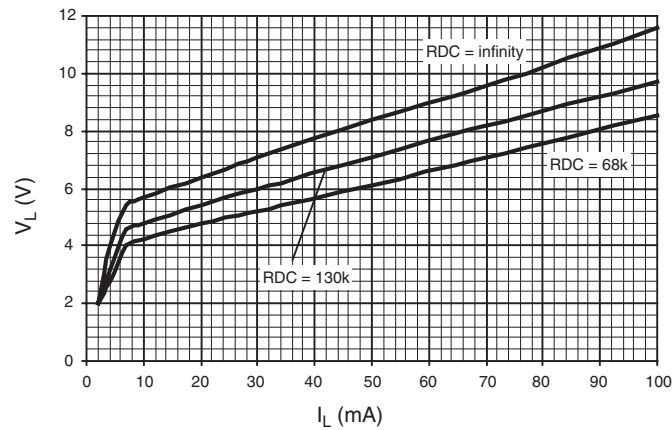


Figure 8-2. Typical Adjustment Range of Transmit Gain

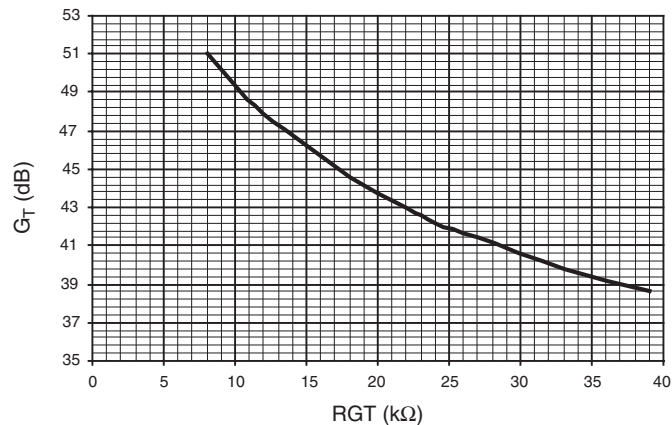


Figure 8-3. Typical Adjustment Range of Receive Gain (Differential Output)

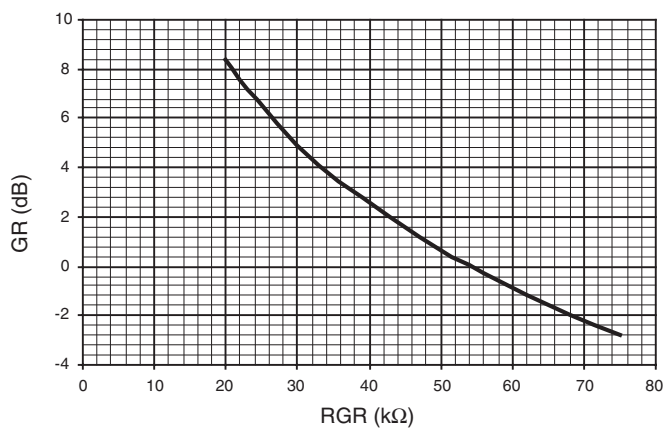


Figure 8-4. Typical AGA Characteristic

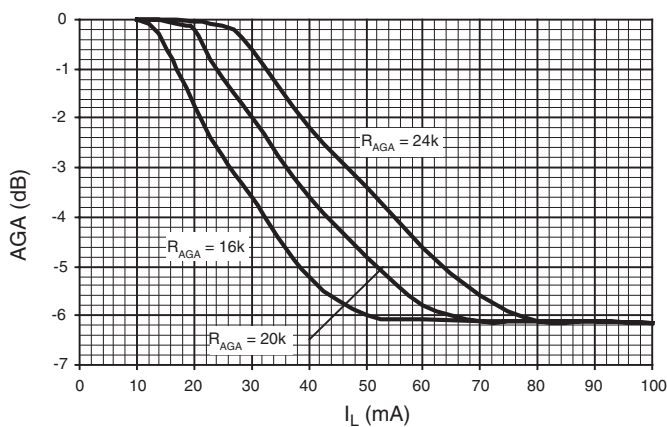
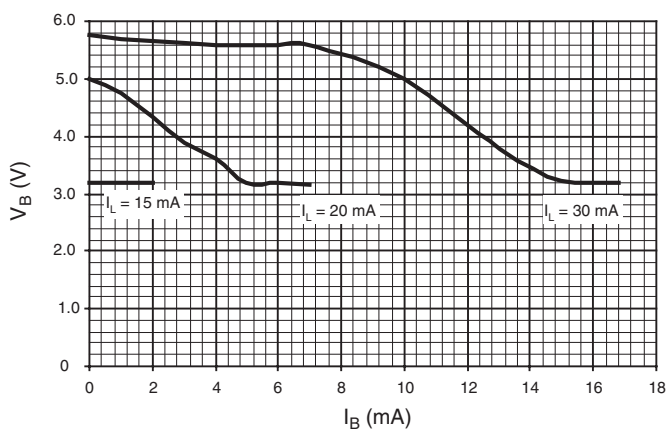
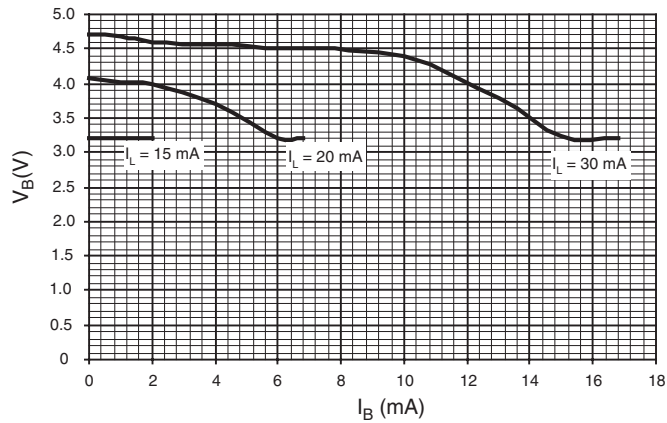


Figure 8-5. Typical Load Characteristic of V_B for a Maximum (RDC = Infinity) DC-characteristic and 3-mW Loudspeaker Output



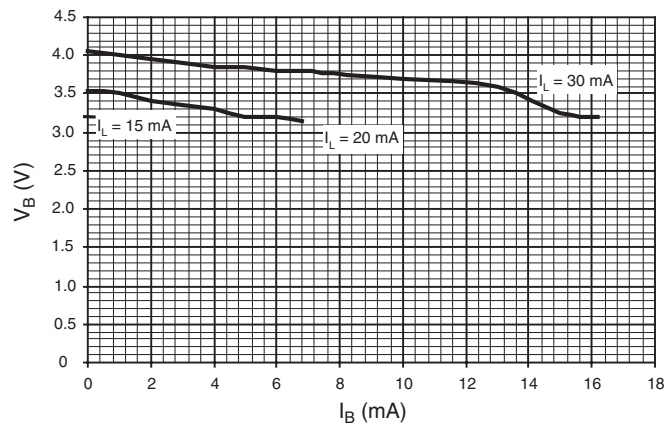
RDC = Infinity; $V_I = 200$ mV/1 kHz; PSAO = 3 mW;
IMP = 2 mA; IM = 300 μ A; RGSA = 560k

Figure 8-6. Typical Load Characteristic of V_B for a Medium DC-characteristic (RDC = 130 k Ω) and 3-mW Loudspeaker Output



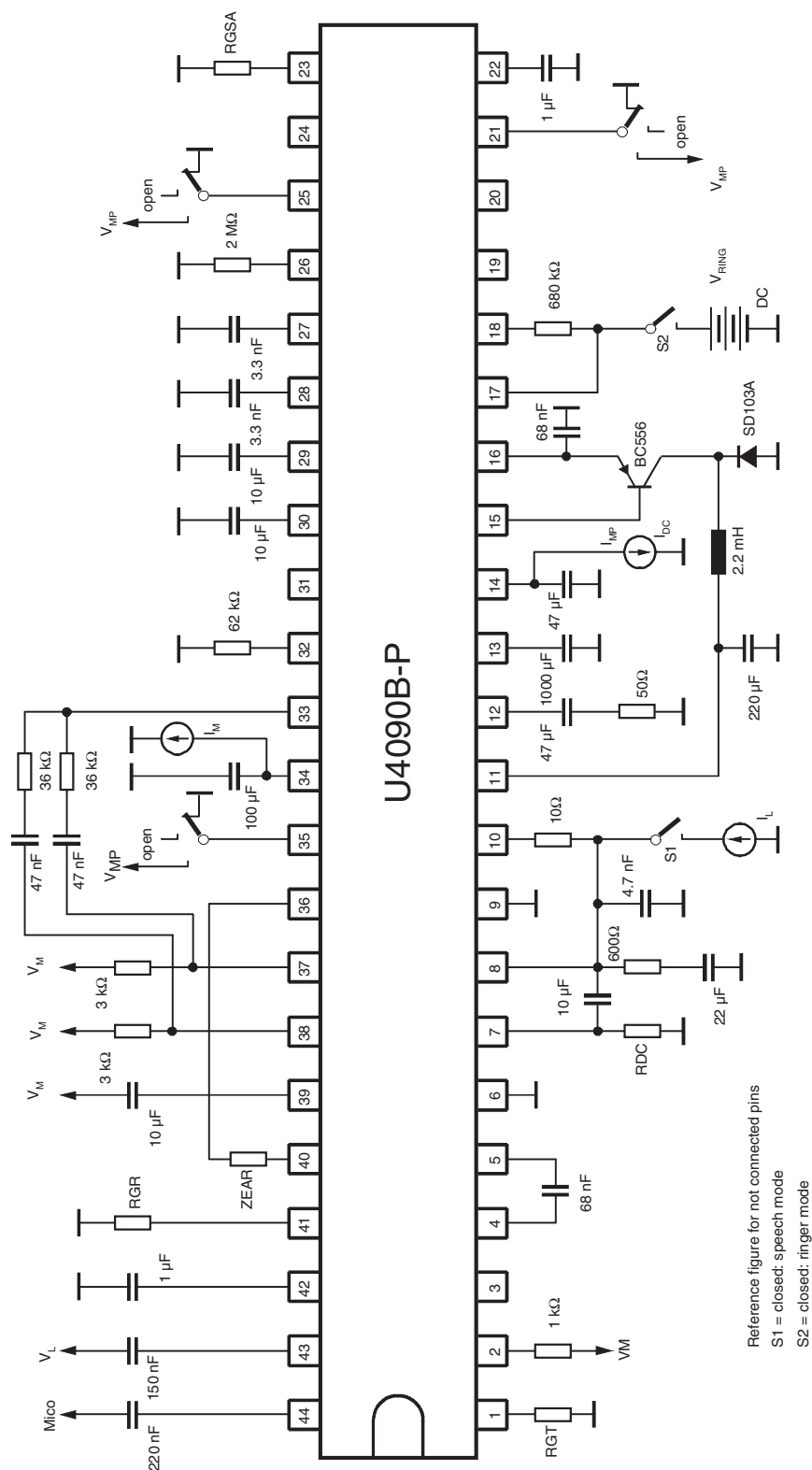
RDC = 130 k; V_I = 200 mV/1 kHz; PSAO = 3 mW;
IMP = 2 mA; IM = 300 μ A; RGSA = 560k

Figure 8-7. Typical Load Characteristic of V_B for a Minimum DC-characteristic (RDC = 68 k Ω) and 3-mW Loudspeaker Output



RDC = 68 k; V_I = 200 mV; PSAO = 3 mW; IMP = 2 mA;
IM = 300 μ A; RGSA = 560k

Figure 8-8. Basic Test Circuit



Reference figure for not connected pins
 S1 = closed: speech mode
 S2 = closed: ringer mode

Figure 8-9. Test Circuit for DC Characteristics and Line Detection

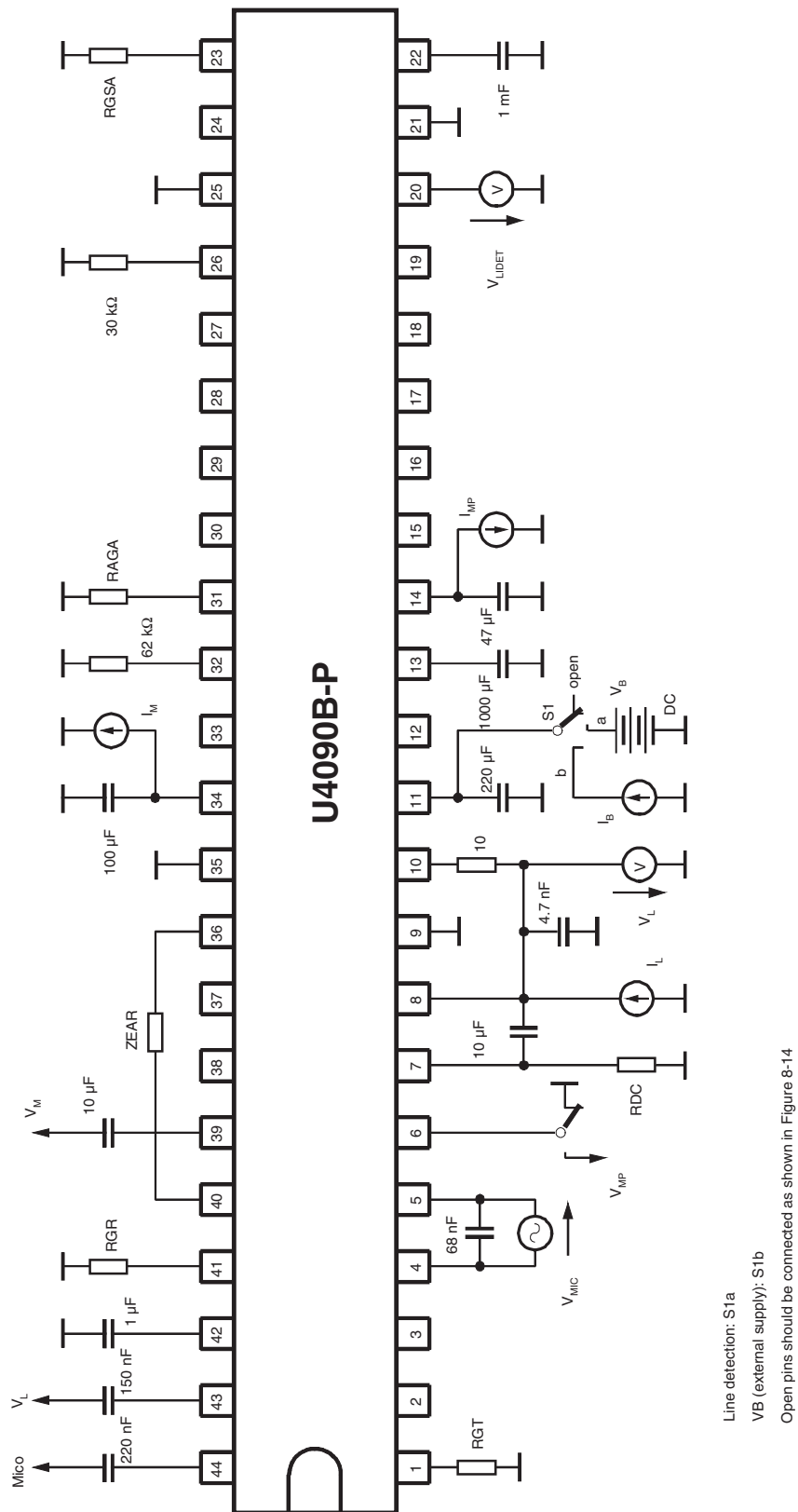


Figure 8-10. Test Circuit for Transmission Amplifier

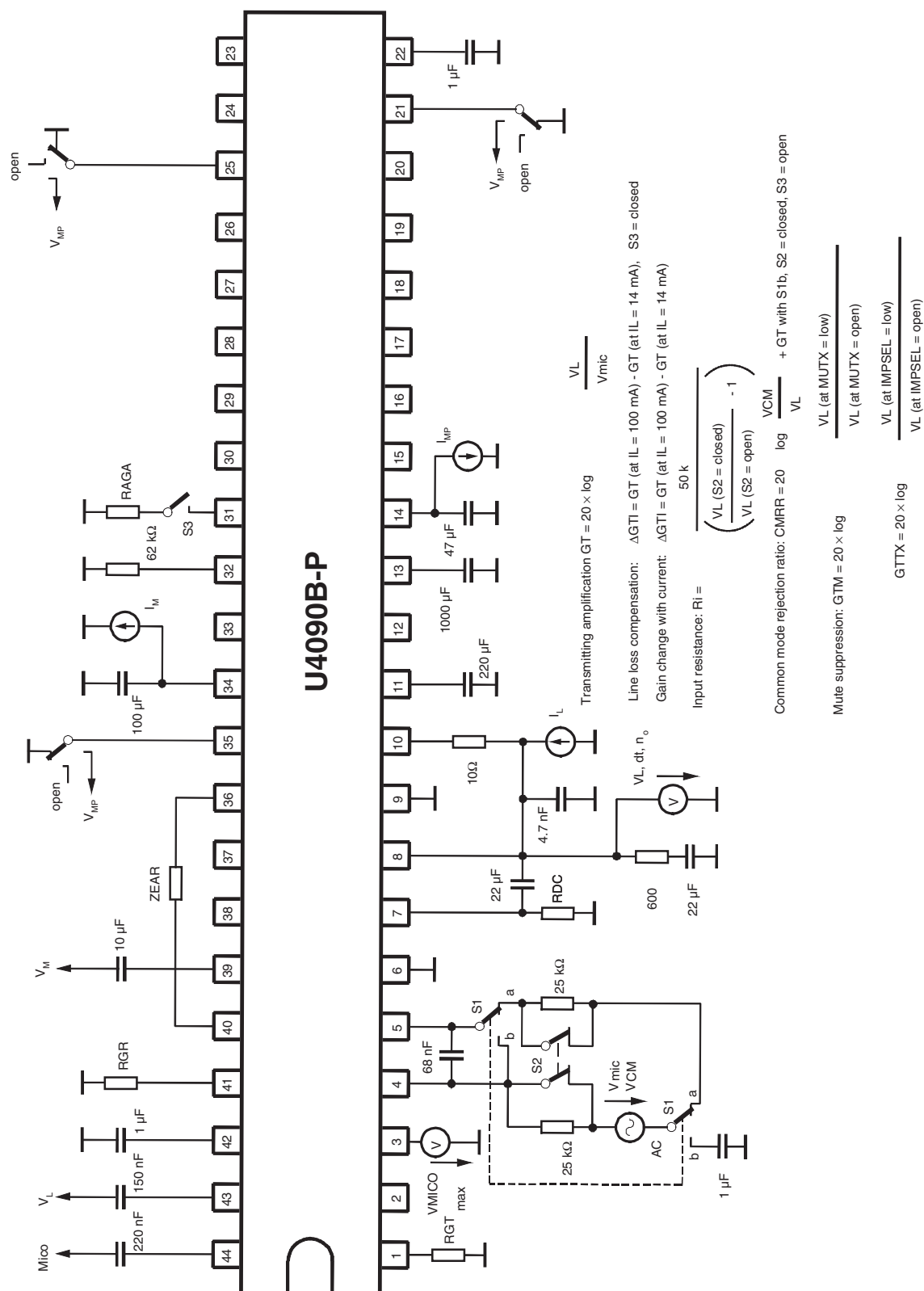
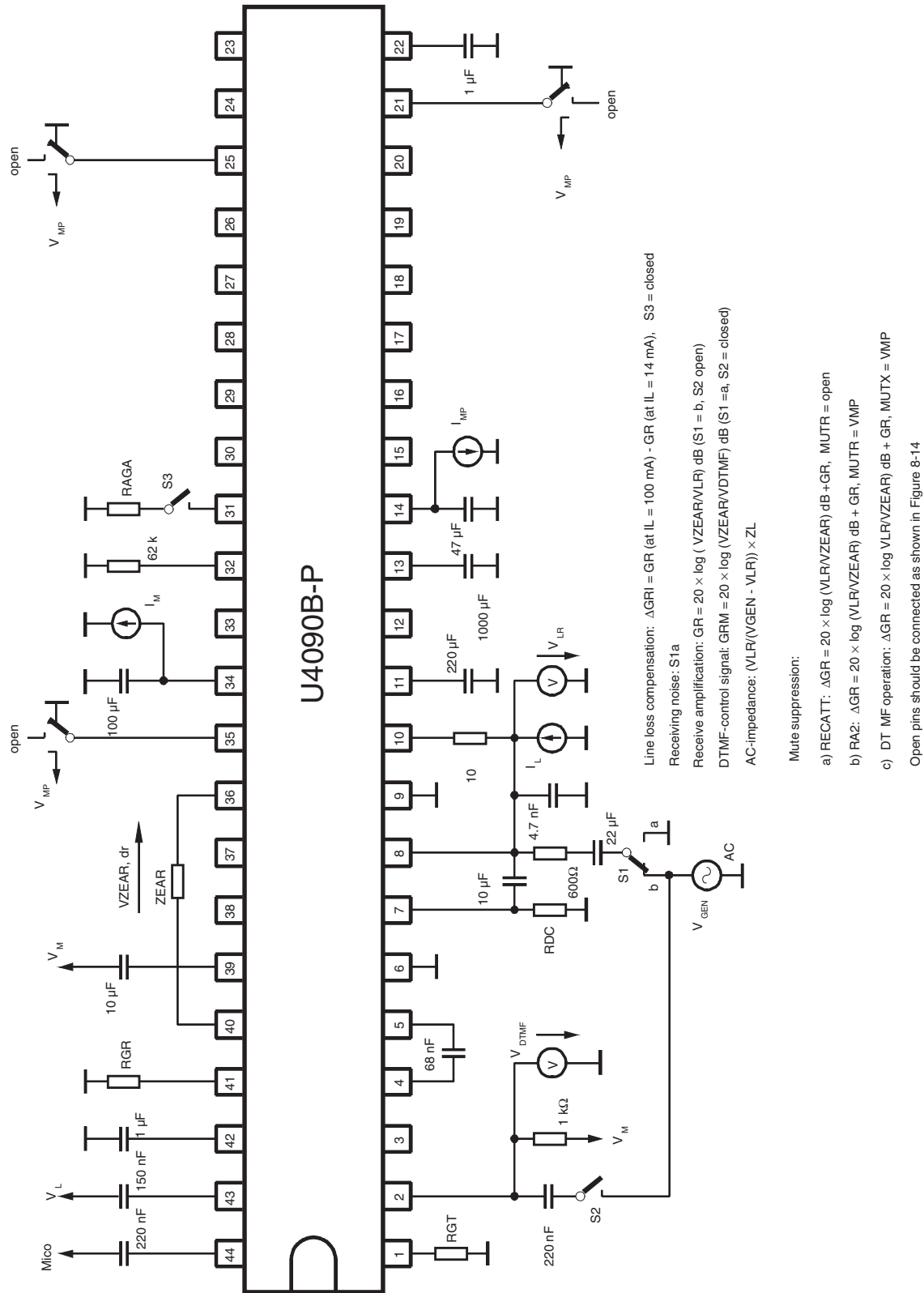
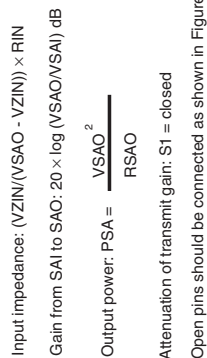


Figure 8-11. Test Circuit for Receiving Amplifier





Attenuation of transmit gain: S1 = closed

Open pins should be connected as shown in Figure 8-14

28



Figure 8-14. Test Circuit for Ringing Power Converter

1) Max. output power: $PSA = \frac{V_{sao}^2}{R_{SAO}}$ (S4 closed)

2) Threshold of ringing frequency detector: detecting VRFDO, when driving VRING from 2V to 22V (VRINGON) and back again (VRINGOFF) (S2 = closed)

3) Input impedance: $RRING = \frac{VRING}{IRING}$ (S3 = closed)

4) Input impedance in speech mode ($I_L > 15 \text{ mA}$): $RRINGSP = \frac{V_{RING}}{I_{RING}}$ (S1 = closed)

5) Ring detector enable: detecting VRFDO, when driving VMP from 0.7V to 3.3V (VMPON) and back again (VMPOFF) (S5, S3 = closed)

Open pins should be connected as shown in Figure 8-14

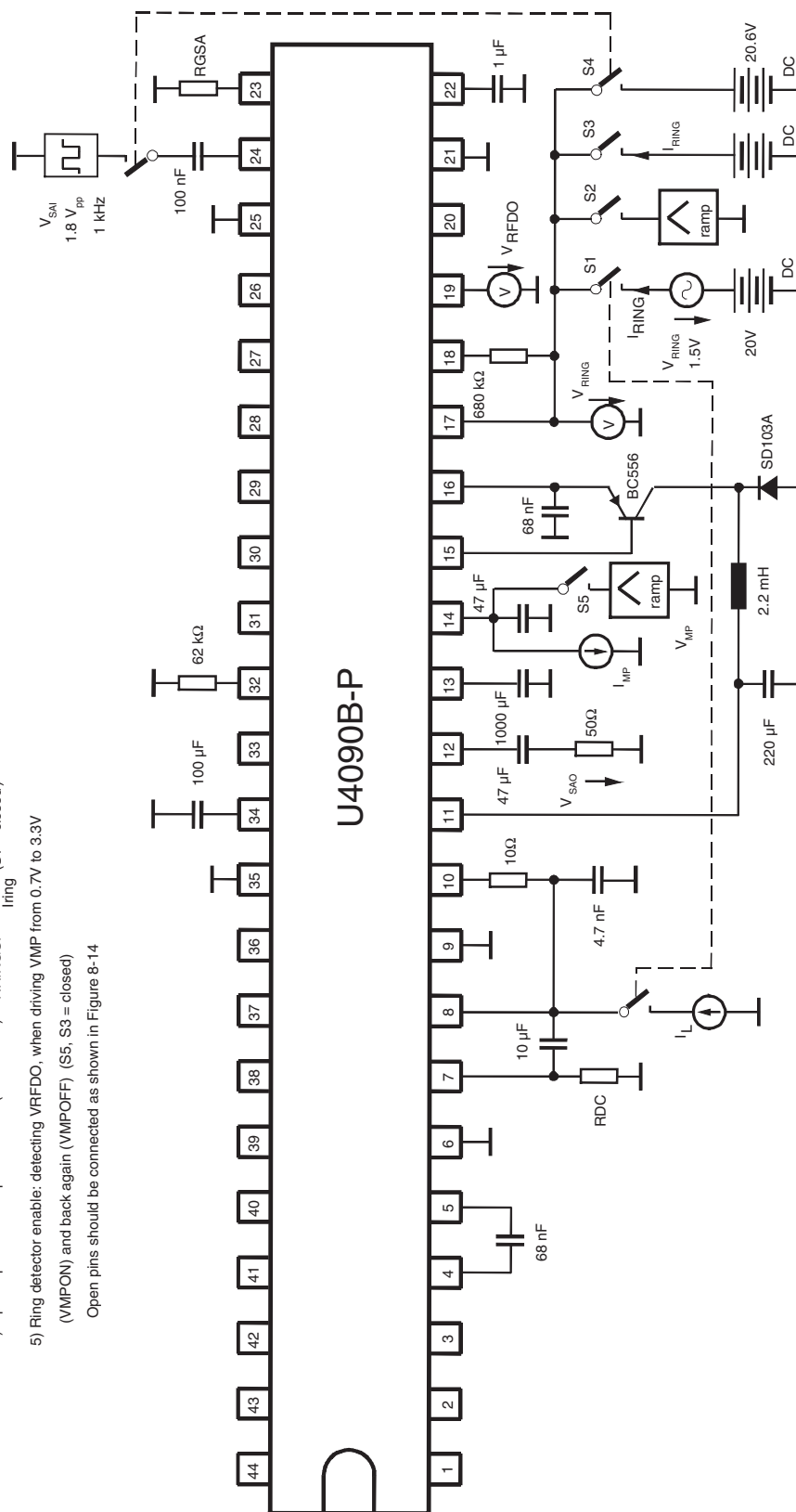
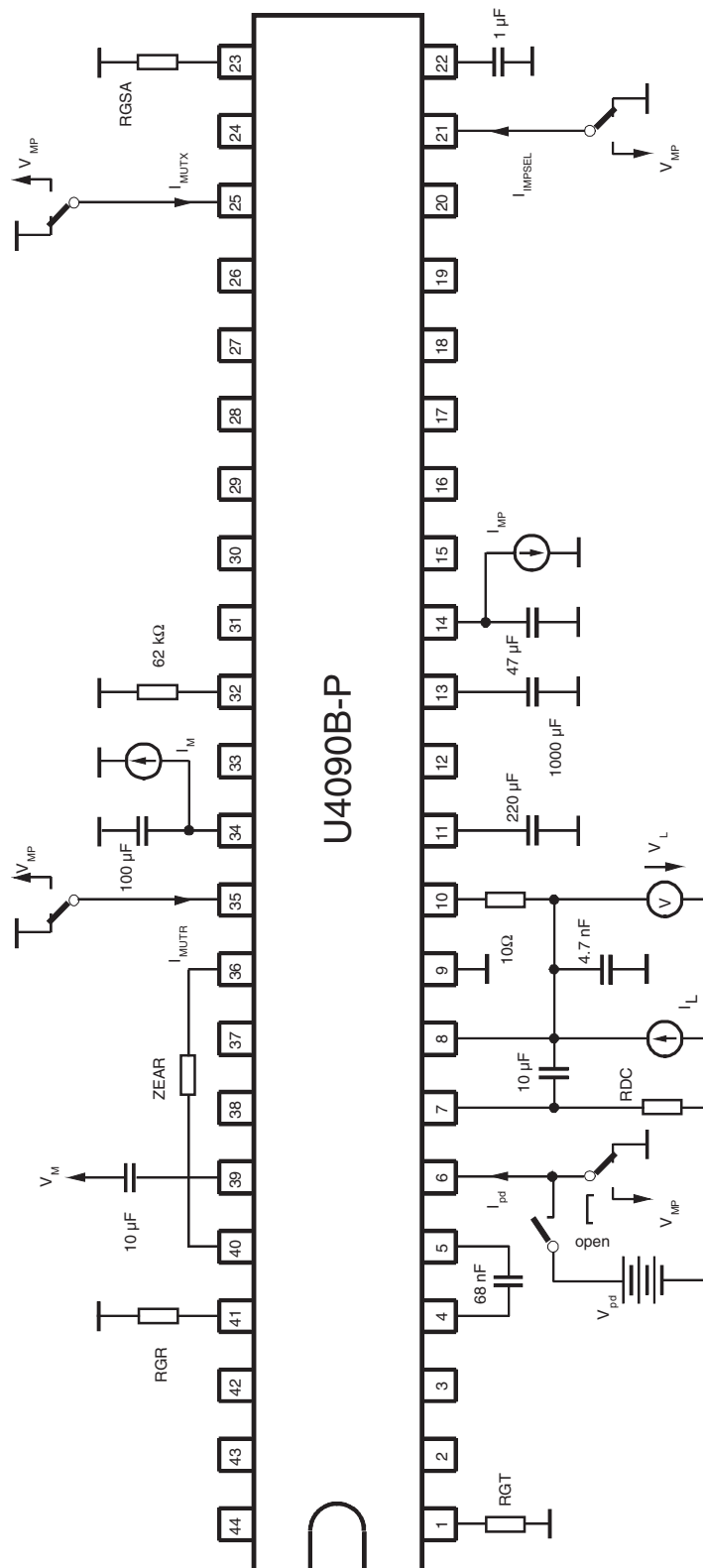


Figure 8-15. Test Circuit for Input Characteristics of I/O Ports



Open pins should be connected as shown in Figure 8-14

Figure 8-16. Application Circuit for Loud-hearing

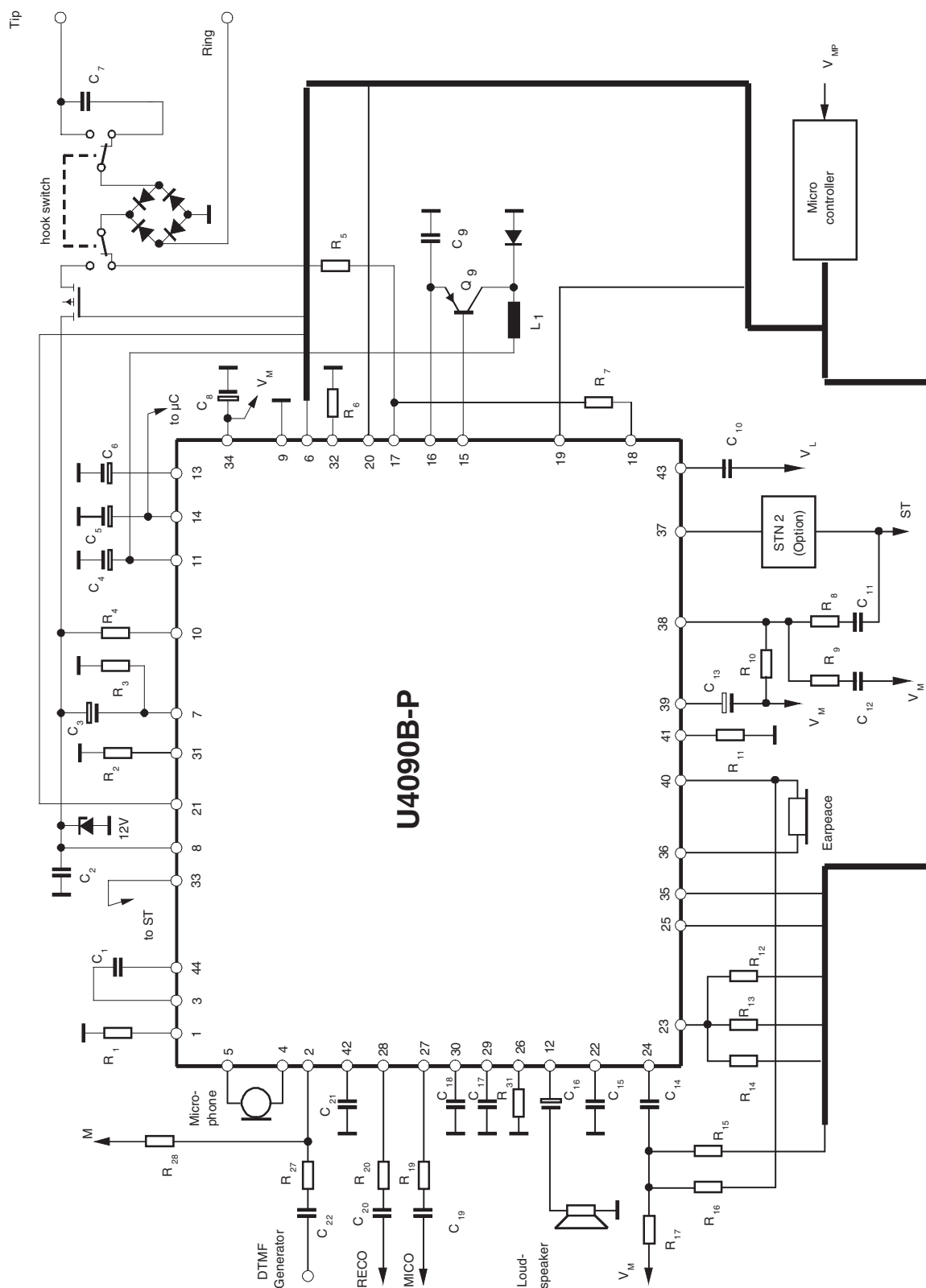


Figure 8-17. Application for Hands-free Operation

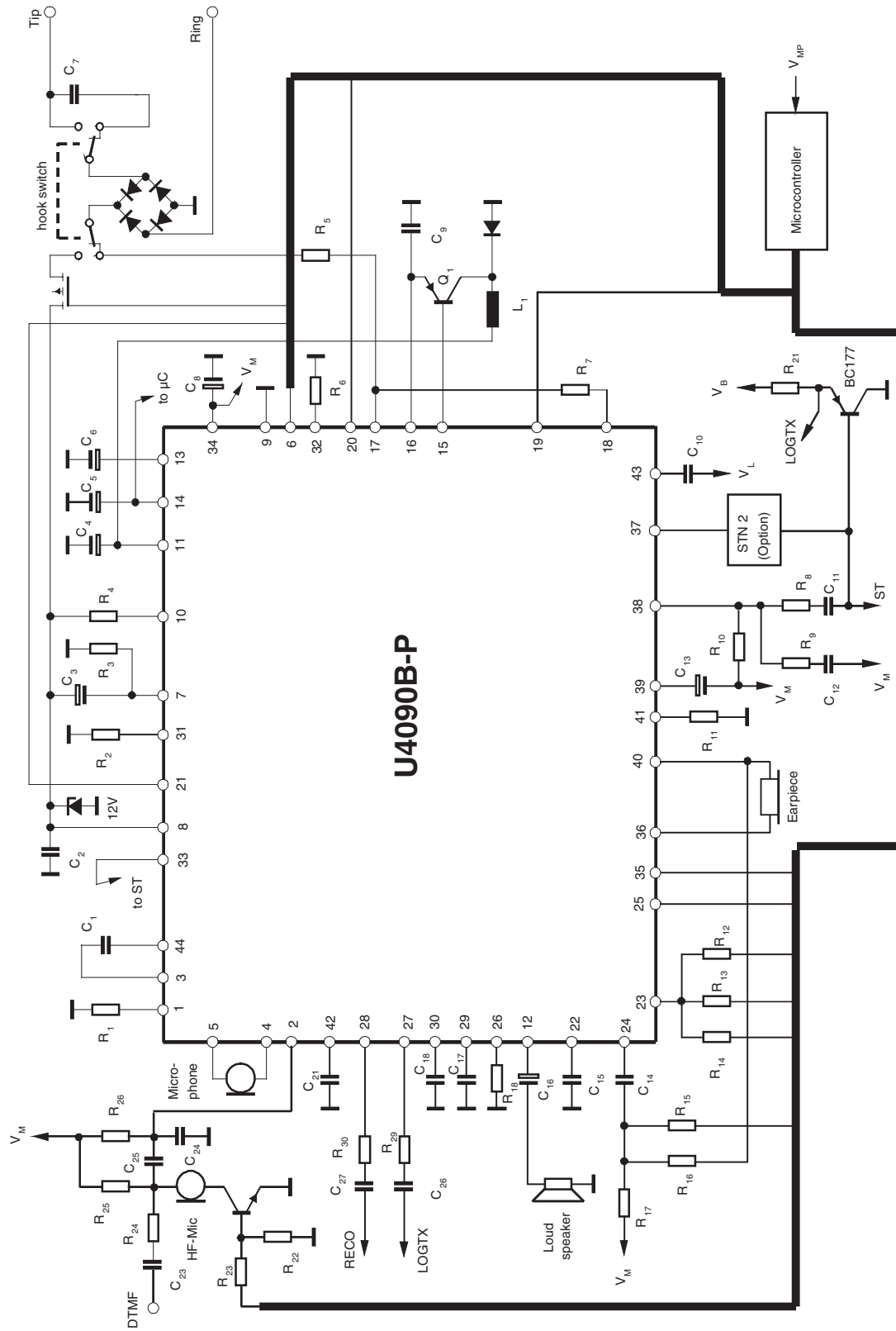


Table 8-1. Typical Values of External Components ([Figure 8-16 on page 31](#) and [Figure 8-17 on page 32](#))

Name	Value	Name	Value	Name	Value	Name	Value
C ₁	100 nF	C ₁₆	47 μ F	R ₃	> 68 k Ω	R ₁₈	30 k Ω
C ₂	4.7 nF	C ₁₇	10 μ F	R ₄	10 k Ω	R ₁₉	6.8 k Ω
C ₃	10 μ F	C ₁₈	10 μ F	R ₅	1.5 k Ω	R ₂₀	6.8 k Ω
C ₄	220 μ F	C ₁₉	68 nF	R ₆	62 k Ω	R ₂₁	15 k Ω
C ₅	47 μ F	C ₂₀	68 nF	R ₇	680 k Ω	R ₂₂	330 k Ω
C ₆	470 μ F	C ₂₁	1 μ F	R ₈	22 k Ω	R ₂₃	220 k Ω
C ₇	820 nF	C ₂₂	100 nF	R ₉	330 k Ω	R ₂₄	68 k Ω
C ₈	100 μ F	C ₂₃	6.8 nF	R ₁₀	3 k Ω	R ₂₅	2 k Ω
C ₉	100 nF	C ₂₄	10 nF	R ₁₁	62 k Ω	R ₂₆	3.3 k Ω
C ₁₀	150 nF	C ₂₅	100 nF	R ₁₂	30 k Ω	R ₂₇	18 k Ω
C ₁₁	86 nF	C ₂₆	470 nF	R ₁₃	62 k Ω	R ₂₈	2 k Ω
C ₁₂	33 nF	C ₂₇	33 nF	R ₁₄	120 k Ω	R ₂₉	1 k Ω
C ₁₃	10 μ F	L ₁	2.2 mH	R ₁₅	47 k Ω	R ₃₀	12 k Ω
C ₁₄	100 nF	R ₁	27 k Ω	R ₁₆	1 k Ω	R ₃₁	56 k Ω
C ₁₅	1 μ F	R ₂	20 k Ω	R ₁₇	1.2 k Ω		

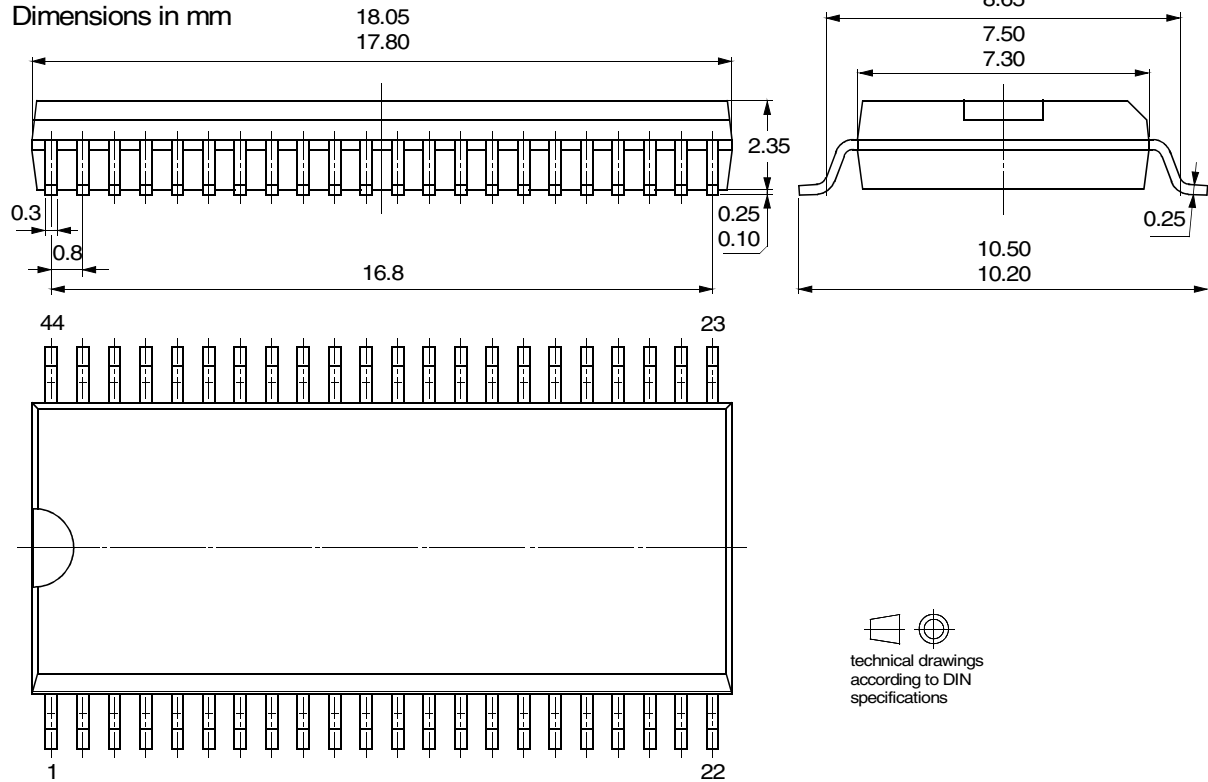
9. Ordering Information

Extended Type Number	Package	Remarks
U4090B-PFNY	SSO44	Pb-free
U4090B-PFNG3Y	SSO44	Taped and reeled, Pb-free
T4090B-PC	Die	Chip on foil

10. Package Information

Package SSO44

Dimensions in mm





Atmel Corporation

2325 Orchard Parkway
San Jose, CA 95131, USA
Tel: 1(408) 441-0311
Fax: 1(408) 487-2600

Regional Headquarters

Europe

Atmel Sarl
Route des Arsenaux 41
Case Postale 80
CH-1705 Fribourg
Switzerland
Tel: (41) 26-426-5555
Fax: (41) 26-426-5500

Asia

Room 1219
Chinachem Golden Plaza
77 Mody Road Tsimshatsui
East Kowloon
Hong Kong
Tel: (852) 2721-9778
Fax: (852) 2722-1369

Japan

9F, Tonetsu Shinkawa Bldg.
1-24-8 Shinkawa
Chuo-ku, Tokyo 104-0033
Japan
Tel: (81) 3-3523-3551
Fax: (81) 3-3523-7581

Atmel Operations

Memory

2325 Orchard Parkway
San Jose, CA 95131, USA
Tel: 1(408) 441-0311
Fax: 1(408) 436-4314

Microcontrollers

2325 Orchard Parkway
San Jose, CA 95131, USA
Tel: 1(408) 441-0311
Fax: 1(408) 436-4314

La Chantrerie
BP 70602
44306 Nantes Cedex 3, France
Tel: (33) 2-40-18-18-18
Fax: (33) 2-40-18-19-60

ASIC/ASSP/Smart Cards

Zone Industrielle
13106 Rousset Cedex, France
Tel: (33) 4-42-53-60-00
Fax: (33) 4-42-53-60-01

1150 East Cheyenne Mtn. Blvd.
Colorado Springs, CO 80906, USA
Tel: 1(719) 576-3300
Fax: 1(719) 540-1759

Scottish Enterprise Technology Park
Maxwell Building
East Kilbride G75 0QR, Scotland
Tel: (44) 1355-803-000
Fax: (44) 1355-242-743

RF/Automotive

Theresienstrasse 2
Postfach 3535
74025 Heilbronn, Germany
Tel: (49) 71-31-67-0
Fax: (49) 71-31-67-2340

1150 East Cheyenne Mtn. Blvd.
Colorado Springs, CO 80906, USA
Tel: 1(719) 576-3300
Fax: 1(719) 540-1759

Biometrics/Imaging/Hi-Rel MPUI
High Speed Converters/RF Datacom
Avenue de Rochepleine
BP 123
38521 Saint-Egreve Cedex, France
Tel: (33) 4-76-58-30-00
Fax: (33) 4-76-58-34-80

Literature Requests

www.atmel.com/literature

Disclaimer: The information in this document is provided in connection with Atmel products. No license, express or implied, by estoppel or otherwise, to any intellectual property right is granted by this document or in connection with the sale of Atmel products. **EXCEPT AS SET FORTH IN ATMEL'S TERMS AND CONDITIONS OF SALE LOCATED ON ATMEL'S WEB SITE, ATMEL ASSUMES NO LIABILITY WHATSOEVER AND DISCLAIMS ANY EXPRESS, IMPLIED OR STATUTORY WARRANTY RELATING TO ITS PRODUCTS INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTY OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT. IN NO EVENT SHALL ATMEL BE LIABLE FOR ANY DIRECT, INDIRECT, CONSEQUENTIAL, PUNITIVE, SPECIAL OR INCIDENTAL DAMAGES (INCLUDING, WITHOUT LIMITATION, DAMAGES FOR LOSS OF PROFITS, BUSINESS INTERRUPTION, OR LOSS OF INFORMATION) ARISING OUT OF THE USE OR INABILITY TO USE THIS DOCUMENT, EVEN IF ATMEL HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.** Atmel makes no representations or warranties with respect to the accuracy or completeness of the contents of this document and reserves the right to make changes to specifications and product descriptions at any time without notice. Atmel does not make any commitment to update the information contained herein. Unless specifically provided otherwise, Atmel products are not suitable for, and shall not be used in, automotive applications. Atmel's products are not intended, authorized, or warranted for use as components in applications intended to support or sustain life.

© Atmel Corporation 2005. All rights reserved. Atmel®, logo and combinations thereof, Everywhere You Are® and others, are registered trademarks or trademarks of Atmel Corporation or its subsidiaries. Other terms and product names may be trademarks of others.



Printed on recycled paper.